

16/C Patent 7/8/3 E Surler

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:

Anand Murthy et al.

U.S. Serial No:

09/475,452

Examiner: Lee, Eugene

Filed:

December 30, 1999)

Art Unit: 2815

For: A NOVEL MOS TRANSISTOR STRUCTURE AND METHOD OF FABRICATION

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT

Dear Sir:

This is in response to the Final Office Action mailed March 21, 2003 and the Request for Continued Examination (RCE) under 37 C.F.R. § 1.114 filed concurrently herewith. Applicant respectfully requests the Examiner to enter this Preliminary Amendment and consider the following remarks.

FIRST CLASS CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage in an envelope addressed to MAIL STOP: RCE
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Attorney Docket: 042390P7794